

Cadence Spectre User Guide

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Virtuoso® Analog Design Environment User Guide

is to teach you to use the Cadence Design Environment to design and test digital CMOS circuits. This manual will walk you through all the necessary steps for designing and testing an inverter. First, we are going to create a schematic for the inverter. We, then, create a symbol for the inverter

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The Cadence main window (Common Interface Window, CIW) and the Library Manager Window are opened. From the CIW menus, all Cadence main tools, online help and options can be accessed. In the window area, all kind of messages (info, errors, warnings, etc) generated by the different Cadence tools appear. You can also introduce commands.

TUTORIAL CADENCE DESIGN ENVIRONMENT

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Open Filezilla, use viterbi-scf1.usc.edu as host and 22 as Port. The username and password are the same as your X-Win32 connection. Navigate and choose files from the left window to upload and files from the right window to download.

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