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*To Microprocessor Microprocessor
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8085 is pronounced as "eighty-eighty-five" microprocessor. It is an 8-bit microprocessor designed by Intel in 1977 using NMOS technology. It has the following configuration ? 8-bit data bus; 16-bit address bus, which can

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address upto 64KB; A 16-bit program counter; A 16-bit stack pointer; Six 8-bit registers arranged in pairs: BC, DE, HL

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Microprocessor 8085 Architecture.

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Microprocessor 8085 Architecture which is shown in the below figure consists of various units and each unit has its respective functionality. In Intel 8085 microprocessor, accumulator acts an 8-bit register to store 8-bit data to perform arithmetic and logical operation on them.

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the 8085 provides a comprehensive treatment of the microprocessor, covering both hardware and software based on the 8085 microprocessor family. The fifth edition, divided into three parts, presents an integrated approach to hardware and software in the context of the 8085

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The 8085/8080A-programming model includes six registers, one accumulator, and one flag register, as shown in Figure. In addition, it has two

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16-bit registers: the stack pointer and the program counter. They are described briefly as follows. The 8085/8080A has six general-purpose registers to store 8-bit data; these are

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8085 Microprocessor Architecture & Working. Generally, the 8085 is an 8-bit microprocessor, and it was launched by Intel team in the year of 1976 with the help of NMOS technology. This processor is the updated version of the microprocessor. The configurations of

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8085 microprocessor mainly include data bus-8-bit, address bus-16 bit, program counter -16-bit, stack pointer-16 bit, registers 8-bit, +5V voltage supply and works at 3.2 MHz single segment CLK.

8085 Microprocessor Architecture,
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Architecture Programming

The Intel 8085 is an 8-bit general-purpose microprocessor. It has an 8-bit data bus. This means that 8 bits of data can flow around in the innards of the microprocessor. Apart from the data bus, it also has a 16-bit address bus, which addresses up to 64KB. It

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also has a 16-bit program counter & a
16-bit stack pointer.

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8085 Microprocessor Architecture and
its Operations: Fig. 1.1 shows the
8085 Microprocessor Architecture. It

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Architecture Programming
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consists of various functional blocks as listed below : Registers; Arithmetic and Logic Unit; Instruction decoder and machine cycle encoder; Address buffer; Address/Data buffer; Incrementer/Decrementer Address Latch; Interrupt control; Serial I/O control

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5.1 Architecture of 8085

Microprocessor . 5.1.1 Register

Section which the program will be used, the suitability of one programming language over . another

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will vary. One ... Architecture Programming

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Intel 8085 is an 8-bit, NMOS microprocessor designed by Intel in 1977. It has following configuration: It is a 40 pin I.C. package fabricated on

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A single LSI chip. The Intel 8085 uses a single +5Vd.c. supply for its operation.

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Here we are going to discuss the
architecture of the 8085

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microprocessor. The 8085 is an 8-bit device. The configuration of the 8085 includes an address bus of 16 bits, a data bus of 8 bits, a stack pointer of 16 bits, the program counter of 16 bits and registers of 8 bits each.

Microprocessor : Architecture, Types,
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Before knowing about the 8085
architecture in detail, lets us briefly

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discuss about the basic features of 8085 processor. 8085 microprocessor is an 8-bit microprocessor with a 40 pin dual in line package. The address and data bus are multiplexed in this processor which helps in providing more control signals. 8085 microprocessor has 1 Non-maskable

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Architecture and 3 maskable interrupts. It provides serial interfacing with serial input data (SID) and serial output data (SOD).

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Programming and Applications ...

This book is designed as a first-level introduction to Microprocessor 8085, covering its architecture, programming, and interfacing aspects. Microprocessor 8085 is the basic processor from which...

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*MICROPROCESSOR 8085:
ARCHITECTURE, PROGRAMMING,
AND ...*

The Intel 8085 ("eighty-eighty-five") is an 8-bit microprocessor produced by Intel and introduced in March 1976. It is a software-binary compatible with the more-famous Intel 8080 with only

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two minor instructions added to support its added interrupt and serial input/output features. However, it requires less support circuitry, allowing simpler and less expensive microcomputer systems to be built.

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